

5904

HART interface module

- Compliant with HART physical layer specifications
- Configurable as primary or secondary master
- Unloads HART protocol details from application software
- Enables up to 4 HART ports per CPU without using CPU serial ports
- 3 year warranty on parts and labor



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Product Data Sheet 5904

Specifications

> 5904 HART interface module

General

Modulation	<ul style="list-style-type: none"> • Bell 202 Frequency Shift Key (FSK) • Mark = 1200 Hz • Space = 2200 Hz
Data Rate	1200 baud
Transmit Level	500mVp-p into 250Ω
Receive Sensitivity	<ul style="list-style-type: none"> • 120mVp-p guaranteed on • 80mVp-p guaranteed off
Output Impedance	<ul style="list-style-type: none"> • 300Ω • Transformer isolated
Input Impedance	<ul style="list-style-type: none"> • 4000Ω • Transformer isolated
Load Resistor	250Ω, 1 Watt maximum
Visual Indicators	<ul style="list-style-type: none"> • TX: Transmitted data LED • RX: Received data LED
Power Requirements	5V @ 20mA
Terminations	4 pole, removable terminal block, 12 to 22 AWG, 15 amp contacts
Dimensions	4.25 inch (108 mm) wide, 4.625 inch (118 mm) high, 1.75 inch (44 mm) deep
Mounting	7.5 x 35 DIN rail
Packaging	Corrosion-resistant zinc-plated steel with black enamel paint
Environment	5% RH to 95% RH, non-condensing, -40°C/F to 70°C /158°F
Safety	cCSA _{US} and cUL _{US} Class I, Division 2 Hazardous Area Rating, UL508 listed
Part Number	TBUX297205

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